

	L #	Hits	Search Text	DBs	Time Stamp
1	L1	26	filter and (soft or half) adj latch	USPAT; EPO; JPO; DERWENT	2003/04/27 17:19
2	L2	22507	filter and latch	USPAT; EPO; JPO; DERWENT	2003/04/27 17:24
3	L3	0	2 and processing adj (unit or module) and input adj gating adj device	USPAT; EPO; JPO; DERWENT	2003/04/27 17:25
4	L4	2756	2 and processing adj (unit or module)	USPAT; EPO; JPO; DERWENT	2003/04/27 17:26
5	L5	2660	4 and input	USPAT; EPO; JPO; DERWENT	2003/04/27 17:26
6	L6	653	5 and input adj (unit or device or circuit)	USPAT; EPO; JPO; DERWENT	2003/04/27 17:27
7	L7	1182	5 and input adj2 (unit or device or circuit)	USPAT; EPO; JPO; DERWENT	2003/04/27 17:27
8	L8	529	7 not 6	USPAT; EPO; JPO; DERWENT	2003/04/27 17:28
9	L9	5	8 and drive adj transistor	USPAT; EPO; JPO; DERWENT	2003/04/27 17:32
10	L10	524	8 not 9	USPAT; EPO; JPO; DERWENT	2003/04/27 17:33
11	L11	12	10 and 365/\$.ccls.	USPAT; EPO; JPO; DERWENT	2003/04/27 17:37
12	L12	512	10 not 11	USPAT; EPO; JPO; DERWENT	2003/04/27 17:38
13	L13	406	12 and @pd<=19990707	USPAT; EPO; JPO; DERWENT	2003/04/27 17:38
14	L14	73	13 and @pd>=19970707	USPAT; EPO; JPO; DERWENT	2003/04/27 18:06
15	L15	1	processing adj2 unit and filter and (soft or half) adj latch	USPAT; EPO; JPO; DERWENT	2003/04/27 18:11
16	L16	1	processing adj (unit or module) and filter and (soft or half) adj latch	USPAT; EPO; JPO; DERWENT	2003/04/27 18:11
17	L19	4	gating adj device and (CPU same logic adj signal)	USPAT; EPO; JPO; DERWENT	2003/04/27 18:55
18	L20	0	gating adj device and (CPU with logic adj signal)	USPAT; EPO; JPO; DERWENT	2003/04/27 18:55